



**PATENT APPLICATION**

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Confirmation No. 2380

MIYAGI

Atty. Dck. No. 100021-00133

Serial No.: 10/674,016

Examiner: Kenneth B. Wells

Filed: September 30, 2003

Art Unit: 2816

For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE HAVING A LEAKAGE CURRENT CUTOFF CIRCUIT, CONSTRUCTED USING MT-CMOS, FOR REDUCING STANDBY LEAKAGE CURRENT

**PRELIMINARY AMENDMENT UNDER 37 CFR § 1.114**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450  
Sir:

Date: March 29, 2005

Responsive to the Office Action dated December 1, 2005, the period for response being extended to April 1, 2005 by the attached one-month Petition for Extension of Time, please amend the application referenced above as follows:

**Amendments to the Claims** are reflected in a listing of the claims, which begins on page 2 of this paper.

**Remarks/Arguments** begin on page 10 of this paper.